Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An electronic circuit, comprising:

a first transistor that ~~becomes on-state when a scan line is selected~~has a first drain, a first source and first gate;

a capacitive element that holds an electrical charge ~~electrical charge amount according to a data signal transmitted from a data line via the first transistor; and~~whose amount corresponds to a data signal supplied through the first transistor when the first transistor is in an on-state;

a second transistor ~~whose conduction state is controlled, based on the electrical charge amount~~that has a second drain, a second source and a second gate and whose conduction state is set according to the amount of the electrical charge held in the capacitive element; ~~element, the second transistor transmitting a current amount corresponding to a conduction state to an electronic element,~~

modes for driving the electronic circuit including at least a first mode and a second mode;~~the electrical charge amount according to the data signal being accumulated in the capacitive element even when either a two-level data voltage or a multilevel data voltage is transmitted as the data signal.~~

a two-level-data signal being supplied as the data signal to the capacitive element in the first mode; and

a multilevel-data signal being supplied as the data signal to the capacitive element in the second mode.

2. (Currently Amended) The electronic circuit according to Claim 1,

the two-level-data ~~voltage~~ signal and the multilevel-data ~~voltage~~ signal being transmitted ~~via~~ through the first ~~switching~~ transistor.

3. (Currently Amended) The electronic circuit according to Claim 1, further comprising:

a third transistor that resets the ~~electrical charge amount~~ electrical charge held in the capacitive element.

4. (Currently Amended) The electronic circuit according to Claim 1, further comprising:

a fourth transistor ~~of which a conductivity is controlled according to the multilevel data voltage, the fourth transistor being~~ that is connected between ~~a~~ the second drain and the second gate.~~gate and a drain of the second transistor, the fourth transistor compensating a threshold voltage of the second transistor.~~

5. (Currently Amended) The electronic circuit according to ~~Claim 1~~Claim 29, further comprising:

a fifth transistor that is connected between the second transistor and ~~that determines the timing of driving~~ the electronic element.

6. (Currently Amended) The electronic circuit according to ~~Claim 1~~Claim 29, the electronic element being an EL element.

7. (Previously Presented) The electronic circuit according to Claim 6, the EL element having a light-emission layer formed of an organic material.

8. (Currently Amended) An electro-optical device, comprising:

a plurality of scan lines;

a plurality of data lines; and

a plurality of unit ~~circuits;~~circuits, each of which includes the electronic circuit according to Claim 1 and the electronic element that functions as an electro-optical element, the first gate being coupled to one scan line of the plurality of scan lines.

~~a first data-voltage output circuit that outputs a two-level-data-voltage as a data signal to each of the plurality of unit circuits via a respective data line of the plurality of data lines;~~

~~a second data-voltage output circuit that outputs a multilevel-data-voltage to each of the plurality of unit circuits via a respective data line of the plurality of data lines.~~

9. (Currently Amended) ~~An~~ The electro-optical device according to Claim 8, the ~~two-level-data voltage~~ two-level-data signal and the ~~multilevel-data voltage~~ multilevel-data signal being ~~transmitted via~~ supplied to each of the plurality of electronic circuits through one ~~and the same~~ data line of the plurality of data lines.

10. (Currently Amended) An electro-optical device according to Claim 8, the ~~two-level-data voltage~~ two-level-data signal and the ~~multilevel-data voltage~~ multilevel-data signal being ~~transmitted via data lines~~ supplied to each of the plurality of electronic circuits through two data lines of the plurality of data ~~lines~~ lines, the two data lines being ~~that are~~ different from each other.

11. (Currently Amended) ~~An~~The electro-optical ~~device, comprising:~~ device according to Claim 8,

~~a plurality of scan lines;~~

~~a plurality of data lines provided so as to cross the scan lines;~~

~~a unit circuit that is provided so as to correspond to each of the intersections of the plurality of scan lines and the plurality of data lines and that transmits a drive current according to a data voltage transmitted via a respective data line of the plurality of data lines to an electro-optical element; and~~

~~a control device that generates and outputs either a two-level-data voltage for applying digital-gray-scale modulation to the electro-optical element or a multilevel-data voltage for applying analog-gray-scale modulation to the electro-optical element, based on image data.~~the two-level-data signal modulating a gray scale of the electronic element by a digital process, and

the multilevel-data signal modulating a gray scale of the electronic element by an analog process.

12. (Currently Amended) The electro-optical device according to Claim 11,

~~the unit circuit comprising:~~

~~a first transistor that becomes on-state when a respective scan line of the plurality of scan lines is selected;~~

~~a capacitive elementthat holds either a two-level-data voltage for digital-gray-scale modulation or a multilevel-data voltage for analog-gray-scale modulation transmitted from a respective data line of the plurality of data lines via the first transistor as an electrical-charge amount; and~~

~~a second transistor whose conduction state is controlled, based on the electrical-charge amount held in the capacitive element, the second transistor transmitting a current amount corresponding to the conduction state to the electro-optical element.~~

the digital process being carried out in the first mode, and

the analog process being carried out in the second mode.

13. (Currently Amended) The electro-optical device according to Claim 12,

~~the unit circuit further comprising:~~

~~a third transistor that resets the electrical-charge amount held in the capacitive element.~~

the digital process being carried out for suppressing electrical power consumption, and

the analog process being carried out for improving display quality.

14. (Currently Amended) The electro-optical device according to Claim 12,

display quality during the second mode being higher than display quality during the first mode.~~the unit circuit further comprising a fourth transistor that compensates a threshold voltage of the second transistor, the fourth transistor being connected between a gate and a drain of the second transistor when the analog gray scale modulation is performed.~~

15. (Currently Amended) The electro-optical device according to ~~Claim 11,~~Claim 32, further comprising:

a switching circuit that controls an electrical connection between the first data output circuit and each of the plurality of data lines and that controls an electrical connection between the second data output circuit and each of the plurality of data lines.

~~the unit circuit further comprising a fifth transistor that determines the timing of driving the electro optical element.~~

16. (Currently Amended) ~~An~~The electro-optical device according to Claim 11,

the ~~electro optical~~ electronic element being ~~an EL element.~~modulated by a time-ratio gray-scale modulation.

17. (Currently Amended) ~~An~~The electro-optical device according to Claim 16,

the driving current whose level corresponds to the two-level data signal being supplied to the electronic element during a predetermined period, and

the driving current being stopped after the predetermined period.~~the EL element having a light emission layer formed of an organic material.~~

18. (Currently Amended) An electronic apparatus comprising the ~~The~~ electro-optical device according to ~~Claim 11,~~Claim 8.

~~the control device generating the two-level-data voltage for applying the digital-gray-scale modulation to the electro-optical element in low-electrical-power-consumption mode and the multilevel-data voltage for applying the analog-gray-scale modulation to the electro-optical element in non-low-electrical-power-consumption mode that drives the electro-optical element.~~

19-28. (Canceled)

29. (New) The electronic circuit according to Claim 1, further comprising:

a driving current whose level corresponds to the conduction state of the second transistor being supplied to an electronic element.

30. (New) The electronic circuit according to Claim 1, each of the two-level-data signal and the multilevel-data signal being a voltage signal.

31. (New) The electronic circuit according to Claim 4, the fourth transistor compensating a threshold of the second transistor.

32. (New) The electro-optical device according to Claim 8, further comprising:

a first data output circuit that outputs the two-level-data signal to the plurality of electronic circuits; and

a second data output circuit that outputs the multilevel-data signal to the plurality of electronic circuits.